VEDA IIT MS in VLSI Engineering,

Dear Students,

VEDA IIT offers **MS in VLSI Engineering at Hyderabad under MoU with JNTUH and at Amaravathi under MoU with JNTUK with the degree awarded by JNTUH and JNTUK respectively.**

It is a full time - 4 semester program in collaboration with Industry consortium - SoCtronics and INVECAS.

The program consists of a multitude of industry relevant subjects & labs. The program has created an enviable track record of near 100% placements for all its batches since inception in 2002.

Companies, which have recruited the students from this stream, have a strong feeling that these students after MS program are far better than those from the premier educational Institutes due to the intensive Industry oriented course training followed by 1 year full time Internship in industry on live projects.

Considering the program structure and success of students, Times of India rated VEDA IIT as one of the top five VLSI Engineering institutes in India.

Admissions for this program offered at Hyderabad and Amaravati are on all India level, based on performance in Common Entrance Test and Interview. If the seats are available after exhausting the qualified candidates in VEDA IIT Entrance test & Interview, GATE qualified candidates can also be considered for admission into this MS in VLSI Engineering program subject to their performance in the interview if they are shortlisted for attending the same. However, they will not be entitled for GATE scholarship.

In this context VEDA IIT has released a common Admission Notification for the current academic year 2018-19. (Poster attached)

Eligibility: BE/ B.Tech. in Electronics/ Electrical/ Computer Science/ IT/ Instrumentation

Note: (i) Final year B.Tech students also may apply. (ii) Those who applied/appeared for other VEDA Test(s) mainly catering to recruitment in the last 6 months also can apply for this program.

Schedule:

Common Entrance test: 26th May 2018
Last date to apply: 22nd May 2018

REQUEST TO THE COLLEGE /UNIVERSITY:
Considering the association we have with your institute through you and your own exemplified efforts to place your students in highly prospective VLSI Engineering core domain, we feel that forwarding this notification to your students may be beneficial particularly to those interested to pursue post-graduate course and those who want to get into VLSI design domain. The attached poster which contains all the details of this MS Admission Test may kindly be uploaded in your college website. Also please circulate the poster among all the concerned students in your database.

For details, visit: www.vedaiit.org Ph: 040-43929999/9989928276;
Mail: careers@vedaiit.com, hr@vedaiit.com .
For details about consortium company visit www.soctronics.com

Thanks and Regards
HR, VEDA IIT

Selected list

S.No	Roll No	UG	Branch	Name	Gender	Mobile Number	Mail ID
1	150040849	B.Tech	ECE	NAVEEN TADEKORU	Male	9515721618	t.naveen.latha@gmail.com